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A low noise amplifier is designed for future applications in the 60GHz band, using an existing SiGe technology, BiCMOS8HP from IBM. Different topologies are analyzed and compared. wTo different schematics of single ended three stage designs are compared. A differential four stage CE topology is designed and simulated with parasitic extraction.

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### ~~Design of a 60GHz Low Noise Amplier in SiGe Technology~~

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### ~~Design of a low power 60GHz OOK receiver in 65nm CMOS ...~~

The most common wireless applications in 1994 were still broadcast TV and cellular systems. Both of these operated in the much lower range of 600MHz to roughly 2GHz bands. Design and operation of 60GHz mmwave systems was very difficult and expensive. The eco-system of components, test gear and more just did not exist.

### ~~60GHz mmwave Explained - Siklu Ltd~~

This architecture has been used in the production of a low-cost, 60-GHz module that supported a data rate greater than 155 Mbps. Variations of the complete chip-set architecture can be used to...

### ~~60 GHz Transceiver Flaunts Low Cost ... - Electronic Design~~

Design and Multiphysics Analysis of Low-Loss 60-GHz SPNT RF-MEMS Switches M. W. Rousstia and M. H. A. J. Herben Eindhoven University of Technology, Department of Electrical Engineering, Electromagnetics Group, P.O. Box 513, 5600 MB, Eindhoven, The Netherlands, +31 (0)40 247 5287 Abstract - A design of a capacitive-shunt RF-MEMS

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Design of 60-GHz low-noise amplifiers with low NF and robust ESD protection in 65-nm CMOS January 2013 IEEE Transactions on Microwave Theory and Techniques 61(1):553-561

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Design and modeling of 60-GHz CMOS integrated circuits. Posted by vaca in 175. Design and Modeling of 60-Ghz CMOS Integrated Circuits. by

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60GHz, the unilateral gain is 8.6dB while the MSG is 6.3dB. For 60GHz circuit design, the reactive component values are very small, requiring inductance values on the order of 50-150pH. Direct implementation of these elements using spiral inductors do not have the required accuracy, so transmission lines are used with the

### ~~ISSCC 2004 / SESSION 24 / TD: WIRELESS TRENDS: LOW POWER ...~~

Design and implementation of a 60GHz digitally-controlled passive phase shifter. It consists of a differential transmission loaded with a differential MOS varactor at each side. It achieves low cost, simple design, low insertion loss, a phase-shift step of 22.5oand a phase shift range of 360oat 60GHz.

### ~~Design methods for 60GHz beamformers in CMOS~~

Modern V-band 60GHz radios from CableFree are easy-to-deploy, cost-effective, wireless Gigabit Ethernet point-to-point bridges operating in the 60 GHz

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millimeter wave V-band, delivering full-duplex capacity of up to 1 Gbit/s over distances of up to 1km or more. Utilising Time Division Duplexing (TDD), the full 2.3Gbps capacity of the radio is available divided into both directions for transmit and receive under user control.

~~60GHz Technology Archives~~ — ~~60GHz Wireless Networks~~

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